Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	547	pci adj controller	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/13 10:41
S2	57	pci adj controllers	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/13 10:54
S3	116	(pci adj controller) and (dma adj controller)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/13 11:04
S4	6	S3 and "714"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/13 11:03
S5	1	(pci adj controllers) and (dma adj controllers)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/13 11:04
S6	1553	dma adj controllers	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/13 11:17
S7	592	(dma adj controllers) and (fault faults fail$5)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/13 11:18
S8	0	(dma adj controllers) and (fault faults fail$5) and "714"/$,ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/13 11:18
S9	69	(dma adj controllers) and (fault faults fail$5) and "714"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/13 13:22

S10	178	"multimaster"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/13 14:52
S11	5	S10 and dma and pci	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/13 14:52
S12	87	714/9.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/11 15:21
S13	9	714/9.ccls. and (system adj controller)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/31 13:39
S14	3234	direct adj memory adj access adj controller	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/31 13:40
S15	8466	"dma" adj controller	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/31 13:40
S16	1091	(direct adj memory adj access adj controller) and (error errors fault faults)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/31 13:40
S17	3656	("dma" adj controller) and (error errors fault faults)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/31 13:40
S18	1006	((direct adj memory adj access adj controller) and (error errors fault faults)) and (dual two multiple)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/31 13:41
S19	680	("dma" adj controller) near6 (dual two multiple)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/31 13:41

S20	135	(direct adj memory adj access adj controller) near6 (dual two multiple)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/31 14:02
S21	5	((direct adj memory adj access adj controller) near6 (dual two multiple)) and "714"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/31 14:02
S22	482	"pci" adj controller	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/31 14:41
S23	16	("pci" adj controller) near6 (dual multiple two)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/31 14:03
S24	18	peripheral adj component adj interconnect adj controller	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/31 14:26
S25	272	("pci" adj controller) same ("pci" adj bus)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/31 14:42
S26	367	("pci" adj controller) and ("pci" adj bus)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/31 14:45
S27	151	(("pci" adj controller) and ("pci" adj bus)) and "710"/$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/31 14:45
S28	430	714/11.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/12 09:02
S29	31	714/11.ccls. and (system adj controller)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/31 16:02

S30	139	714/12.ccls. not 714/11.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/12 09:35
S31	237	714/13.ccls. not 714/11,12.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/12 09:40
S32	630	714/48.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	.2004/07/12 09:47
S33	68	address adj arbiter	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/31 18:39
S34	3894	(error) near6 interrupt	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/01/31 18:39
S35	63	(error adj message) near6 interrupt	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/02/02 12:12
S36	90	714/9.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/12 08:11
S37	468	714/11.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/11 15:25
S38	153	marisetty	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/12 09:32
S39	0	"suresh marisetty"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/12 09:33

S40	0	suresh adj marisetty	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/12 09:33
S41	4888	suresh marisetty	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/12 09:33
S42	8	marisetty-suresh.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/07/12 09:33
S43	144	714/12.ccls. not 714/11.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/11 15:32
S44	261	714/13.ccls. not 714/11,12.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/11 15:33
S45	686	714/48.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/11 15:35
S48	19	714/9.ccls. and @pd>="20030529"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/11 15:21
S49	38	714/11.ccls. and @pd>"20040429"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/11 15:25
S50	9	(714/12.ccls. not 714/11.ccls.) and @pd>="20040629"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/11 15:32
S51	34	(714/13.ccls. not 714/11,12.ccls.) and @pd>="20040624"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/11 15:34

S52	74	714/48.ccls. and @pd>="20040624"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/11 15:52
S53	143	(system adj controller) and (error adj recovery) and (processors (multi-processor mult-processors))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/11 16:08
S54	16	(system adj controller) and (error adj recovery) and (processors (multi-processor mult-processors)) and (idle near6 processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/01/11 16:09